



SREE VAAGESWARI EDUCATIONAL SOCIETY
VAAGESWARI COLLEGE OF ENGINEERING

(Affiliated to JNTUH, Hyderabad.)

(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

Date: 28/04/2021

To
The Principal,
Vaageswari College of Engineering,
Karimnagar.


Subject: Request Proposal to Conduct Five day FDP on **“Digital System Design using DSCH and Microwind”**-Reg.

Respected Sir,

In Accordance to Subject cited above I, Head of the Department of Electronics and Communication Engineering here with request with this letter to permit us to conduct a Five day Faculty Development Program on **“Digital System Design using DSCH and Microwind”** during **17-05-2021 to 21-05-2021**. I also request you to provide all the required facilities.

Thanking You,

Yours Sincerely


HOD-Dept. of ECE.



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CIRCULAR

Date: 03-05-2021

It is hereby informed to **Department of Electronics and Communication Engineering** that a five-day Faculty Development Program will be held on “**Digital System Design using DSCH and Microwind**” from 17th to 21st May 2021. All the teaching and non-teaching members should avail this opportunity to upgrade the technical skills.

Hence, all teaching and non-teaching members should attend the above FDP program without fail.

Copies to:

- Circulate among all the staff members
- All the Notice Boards
- All HODs
- Office File


Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527,

WORKSHOP CONTENT:

Over the past several years, Silicon CMOS technology has become the dominant fabrication process for relatively high performance and cost effective VLSI circuits. The revolutionary nature of these developments is understood by the rapid growth in which the number of transistors integrated on circuit on single chip. In this program we are providing concept of MOS integrated circuits and implementation of several simple and complex Digital circuits with the use of DSCHE software for schematics and microwind software for layouts.

- VLSI Design
- Introduction
- VLSI Design Flow
- Design Hierarchy-Structural
- Full Custom Design
- MOS Transistor
- Structure and working of a MOSFET
- MOS Inverter
- Circuit, Operation and Description
- CMOS Logic Circuits
- CMOS Two input NOR Gate
- Layout of CMOS 2-input NOR Gate
- CMOS Two-input NAND Gate
- Complex CMOS Logic Gates
- Sequential MOS Logic Circuits
- SR Latch based on NOR Gate
- SR Latch based on NAND Gate
- Clocked SR Latch
- Clocked JK Latch
- CMOS D Latch Implementation

Every session is followed by a laboratory session to implement several digital circuits as schematics and layouts as well.

ADVISORY COMMITTEE

CHIEF PATRON

Dr. G. Sreenivas Reddy
General Secretary and Correspondent

PATRONS

Sri.D. Srinivas Reddy
Joint Secretary
Dr. Ch. Srinivas
Principal

COORDINATOR

Mr. A.Venkata Reddy
Associate Professor, Dept. of ECE

ORGANIZING COMMITTEE

Mrs. E. Jyothi Reddy
Mr.A. Arun Kumar
Mr. G.S. Arun Kumar
Mr. T.nageshwar Rao
Mr.D.Laxminarayana
Mr. P. Ajay Kumar
Mrs. K. Jyothsna
Mr. K. Vijay Kumar
Mr. B. Thirupathi

CONVENOR

Mr. B. Ram Mohan Reddy

Associate Professor
Department of Electronics and Communication
Engineering

Email: s4.ecehod@gmail.com
Phone: +91-9959000157

[Signature]
Principal

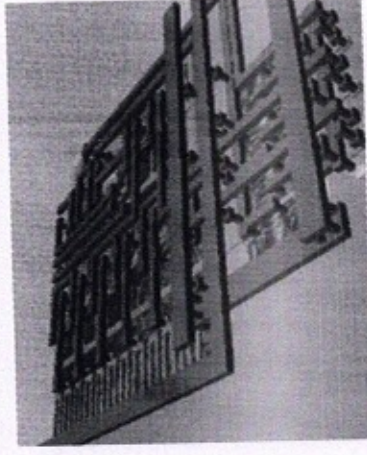
Vaageswari College of Engineering
KARIMNAGAR-505 527.



FIVE-DAY FACULTY DEVELOPMENT PROGRAM

ON

DIGITAL SYSTEM DESIGN USING DSCHE AND MICROWIND



Organized by

Department of Electronics and
Communication Engineering

VAAGESWARI COLLEGE OF
ENGINEERING

Karimnagar, Telangana- India

17th to 21st May, 2021

ABOUT THE INSTITUTE:

Established in 2003, Vaageswari Educational Society is one of the most prestigious society providing high quality professional education in Karimnagar district of Telangana. The society is running multiple educational institutions like Vaageswari College of Engineering (2005), Pharmacy (2003), Pharmaceutical Sciences (2007), Management Sciences (2008). The society was established with a great vision to provide quality education on par with global standards to the students from all over India in general and in particular those from local and rural areas. Society is maintaining high standards so as to make them technically competent and ethically strong individuals who shall be able to improve the quality of life and become an asset to the economy of our country. The Society not only celebrates freedom of thought, cultivates vision and encourages growth, but also inculcates human values and concern for the environment and the society. The institution is awarded with Best Chapter of ISTE AP Section.

VISION:

"To develop the Sree Vaageswari Educational Society into a center of excellence in imparting professional and technical education, to inculcate knowledge and power to the younger generation by providing high quality professional education on par with global standards and to instill high levels of discipline among student community making them ethically strong citizens capable of contributing to the development of society and mankind".

PROGRAMS OFFERED:

The college offers B.Tech programmes in CSE, EEE, ECE, Civil and Mechanical Engineering. The college also offers MCA and M.Tech in Computer Science and Engineering.

ABOUT THE DEPARTMENT:

The department of Electronics and Communication Engineering (ECE) was started in 2005 with an intake of 60 and in a short span of time the intake increased to 180.

The department comprises of well experienced and dedicated faculty with vast experience in teaching. The faculty members have been meticulously and judiciously picked up to impart quality education and inculcated all-round development in the Students. The department has well established labs with state of art hardware equipments and advanced software's.

The Department's main objective is not only make the students excel in theory but also in experimental research through academic projects in various fields. The Department regularly organizes several programs such as workshops, guest lectures, seminars etc. to impart state of art knowledge in different technologies to the students as well as faculty members. Department is dedicated to advancing, disseminating and exploiting knowledge in engineering and technology. The department offers a four-year course a Bachelor's degree in Electronics and Communication Engineering. Department has the most advanced art of advanced equipment in the laboratory. All employees can reach students for any of their problems, whether academic or non-academic. The department aims to technically develop students and make them mentally strong to meet future challenges.

The department also seeks to provide its students with excellent educational and educational opportunities in the field of electronics and communications engineering, thus providing a lasting technical basis in an environment of rapid technical change. In the process, it enables and promotes their professional growth through contact with best professional practices. Consequently, the administration is trying to play the role of resources and technical leadership in development activities at the national level.

Principal
Vaageswari College of Engineering

REGISTRATION FORM:

Please fill this form and send it to us by email to s4.ecehod@gmail.com before 15th May 2021

Name of the Participant: _____

Designation: _____

Department: _____

Organization name and Address: _____

Mobile Number: _____

Email id: _____

Signature and Seal of the
Head /Director of the Institute

NO REGISTRATION FEE





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INVITATION LETTER

To,

Dr. Pankaj Hivraj Rangaree,

Associate Professor,

Department of Electronics and Communication Engineering,

Vaagdevi College of Engineering,

Warangal-506005.

Sub: Invitation to be a resource person for five day Faculty Development Program on "**Digital System Design using DSCH and Microwind**"-regarding.

Dear Sir,

On behalf of Vaageswari College of Engineering, I cordially invite you to deliver a lecture on **Digital System Design using DSCH and Microwind** which will be held from 17th to 21st May 2021. I would be thankful if you share your expertise on the topic with our faculty.

I will be eagerly looking forward to hear from you and hope that you would accept our invitation for the event.

Accepted & Acknowledged

Dr. Ch. Srinivas,

Principal,

Vaageswari College of Engineering,

Karimnagar.

Email: s4.principal@gmail.com

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



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INVITATION LETTER

To,

Dr. Hemant Kumar Gupta,

Assistant Professor,

Department of Electronics and Communication Engineering,

Vaagdevi College of Engineering,

Warangal-506005.

Sub: Invitation to be a resource person for five day Faculty Development Program on “**Digital System Design using DSCH and Microwind**”-regarding.

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accepted & acknowledged
[Signature]

[Signature]
Dr. Ch. Srinivas,
Principal,

Vaageswari College of Engineering,
Karimnagar.

Email: s4.principal@gmail.com

Principal
Vaageswari College of Engineering
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INVITATION LETTER

To,

Mr. Hameed Pasha Mohammad,

Associate Professor and Head,

Department of Electronics and Communication Engineering,

Jayamukhi Institute of Technological Sciences,

Warangal-506332.

Sub: Invitation to be a resource person for five day Faculty Development Program on “**Digital System Design using DSCH and Microwind**”-regarding.

Dear Sir,

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Email: s4.principal@gmail.com

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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

SPEAKER PROFILES

Dr. Korlapati Keerti Kumar, completed his Ph.D from National Institute of Technology, Warangal in the year 2016. Currently he is working as an Associate Professor at Vaageswari College of Engineering. His research interests include Microelectronics, Digital Design and Device Modeling.

Mr. B. Bhargavendra, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Associate Professor at Vaageswari College of Engineering. His research interests include Digital Design and MEMS.

Mr. Adama Venkat Reddy, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Associate Professor at Vaageswari College of Engineering. His research interests include Advanced Wireless Communication Systems.

Mr. B. Ram Mohan Reddy, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Associate Professor at Vaageswari College of Engineering. His research interests include RF IC Design and Device Modeling.

Mr. Bajjuri Thirupathi, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Assistant Professor at Vaageswari College of Engineering. His research interests include VLSI Design and MEMS.

Department of Electronics and Communication Engineering
Five-Day Faculty Development Program on
Digital System Design Using DSCH and Microwind
 (17/05/2021 to 21/05/2021)

SCHEDULE

Date	10.00 AM to 11.30 PM	11.45 AM to 12.45 PM	02.00 PM to 03.30 PM	03.45 PM to 04.45 PM
17/05/2021	Registration and Inaugural	Fundamentals of VLSI Design and Design Flow (KKK)	Introduction to DSCH and Microwind (BRR)	Example Simulations (Lab) (AVR)
18/05/2021	Full Custom IC Design (AVR)	MOS Transistor (KKK)	Simulation of Basic Logic Gates using DSCH (Lab) (BT)	Layout Implementation of Basic Logic Gates using Microwind (Lab) (BRR)
19/05/2021	Digital Design Fundamentals (AVR)	CMOS Implementation of Circuits (KKK)	Simulation of Adder Circuits using DSCH (Lab) (BT)	Layout Implementation of Adder Circuits using Microwind (Lab) (BB)
20/05/2021	Combinational Logic (BRR)	Complex CMOS Logic (KKK)	Simulation of Complex functions using DSCH (Lab) (BT)	Layout Implementation of Complex functions using Microwind (Lab) (AVR)
21/05/2021	Latches and Flip Flops (BB)	Latches and Flip Flops (Contd.) (BB)	Simulation of basic Flip Flops using DSCH (Lab) (BT)	Valedictory

KKK- Dr. Korlapati Keerti Kumar, AVR- Adama Venkata Reddy, BB- Boddu Bhargavendra, BT- Bajjuri Thirupathi, BRR- Banda Ram Mohan Reddy


Coordinator


H.O.D



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Department of Electronics and Communication Engineering

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Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

LIST OF PARTICIPANTS

S.No	Name	Department	Signature
1	Dr. Korlapati Keerti Kumar	ECE	kkk
2	A.Venkata Reddy	ECE	W
3	B.Bhargavendra	ECE	Byr
4	Ajay Kumar Parupelli	ECE	P. Ajakh
5	Kalakonda Jyothsna	ECE	quok
6	T.Nageshear Rao	ECE	NR
7	Arun Kumar Akula	ECE	Arun
8	E. Jyothi Reddy	ECE	Eji
9	Banda Ram Mohan	ECE	Ram
10	G.S. Arun Kumar	ECE	Uff
11	D.Laxminarayana	ECE	Duf
12	K. Vijay Kumar	ECE	Vijay
13	Bajjuri Tirupathi	ECE	mplu
14	Madasu Kranthi Kumar	ECE	Kranthi
15	B.Pooja	ECE	Pooja
16	Gaddamedi Pratheepa	ECE	Pratheepa
17	Lakke Prakash	ECE	As

Coordinator

HOD

Principal

Vaageswari College of Engineering
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(17/05/2021 to 21/05/2021)

ATTENDANCE SHEET

S.No	Name	17/05/21	18/05/21	19/05/21	20/05/21	21/05/21
1	Dr. Korlapati Keerti Kumar	kkk	kkk	kkk	kkk	kkk
2	A.Venkata Reddy	vy	vy	vy	vy	vy
3	B.Bhargavendra	Bur	Bur	Bur	Bur	Bur
4	Ajay Kumar Parupelli	P.Ajak	P.Ajak	P.Ajak	P.Ajak	P.Ajak
5	Kalakonda Jyothsna	juof	juof	juof	juof	juof
6	T.Nageshear Rao	EN	EN	EN	EN	EN
7	Arun Kumar Akula	Anu	Anu	Anu	Anu	Anu
8	E. Jyothi Reddy	Eri	Eri	Eri	Eri	Eri
9	Banda Ram Mohan	Ram	Ram	Ram	Ram	Ram
10	G.S. Arun Kumar	ys	ys	ys	ys	ys
11	D.Laxminarayana	Dy	Dy	Dy	Dy	Dy
12	K. Vijay Kumar	Vijay	Vijay	Vijay	Vijay	Vijay
13	Bajjuri Tirupathi	mpu	mpu	mpu	mpu	mpu
14	Madasu Kranthi Kumar	Kranthi	Kranthi	Kranthi	Kranthi	Kranthi
15	B.Pooja	Pooja	Pooja	Pooja	Pooja	Pooja
16	Gaddamedi Pratheepa	Pratheepa	Pratheepa	Pratheepa	Pratheepa	Pratheepa
17	Lakke Prakash	Pg	Pg	Pg	Pg	Pg

Coordinator

HOD

Principal

Principal

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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

FEEDBACK

Name of the Participant: Dr. K. Keerthi Kumar

Designation: professor

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

- | | | | | | |
|---|---|---|---|---|---|
| 1. The speakers presented materials clearly and concisely. | 5 | 4 | 3 | 2 | 1 |
| 2. The speakers covered the material sufficiently. | 5 | 4 | 3 | 2 | 1 |
| 3. The information presented was relevant to your needs and expectations. | 5 | 4 | 3 | 2 | 1 |
| 4. Sufficient opportunity was provided for questions. | 5 | 4 | 3 | 2 | 1 |
| 5. You were pleased with the presentations. | 5 | 4 | 3 | 2 | 1 |

KKK
Signature



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FEEDBACK

Name of the Participant: A. VenkataReddy

Designation: Assoc Prof.

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

1. The speakers presented materials clearly and concisely.

5 4 3 2 1

2. The speakers covered the material sufficiently.

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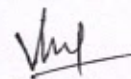
5 4 3 2 1

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Signature



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FEEDBACK

Name of the Participant: B. Bhargavendra

Designation: Assoc. Prof.

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

1. The speakers presented materials clearly and concisely.

5 4 3 ✓ 2 1

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B. Bhargavendra
Signature



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FEEDBACK

Name of the Participant: P. Ajay Kumar

Designation: Asst. Prof

Please respond to the following items by using the scale below:

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FEEDBACK

Name of the Participant: K. Jyothsna

Designation: Asst. Prof

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SuoE
Signature



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FEEDBACK

Name of the Participant: T. Nageshwal Rao

Designation: Associate Professor

Please respond to the following items by using the scale below:

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FEEDBACK

Name of the Participant: A. Arun Kumar

Designation: ASST. PROF.

Please respond to the following items by using the scale below:

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Arun
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Name of the Participant: E. Syalhi

Designation: Associate professor

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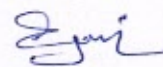
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Designation: Asst. Prof

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Ram
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FEEDBACK

Name of the Participant: G.S. Arun Kumar

Designation: ASST. PROF

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Signature



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(Affiliated to JNTUH, Hyderabad.)

(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

FEEDBACK

Name of the Participant: D. Laxminarayana

Designation: Asst. prof

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

1. The speakers presented materials clearly and concisely.

✓ 5 4 3 2 1

2. The speakers covered the material sufficiently.

5 4 3 2 1

3. The information presented was relevant to your needs and expectations.

✓ 5 4 3 2 1

4. Sufficient opportunity was provided for questions.

5 4 3 2 1

5. You were pleased with the presentations.

5 4 3 2 1


Signature



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

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(17/05/2021 to 21/05/2021)

FEEDBACK

Name of the Participant: K. Vijay Kumar

Designation: Asst. prof

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

- | | | | | | |
|---|---|-----|-----|-----|---|
| 1. The speakers presented materials clearly and concisely. | 5 | 4 | 3 ✓ | 2 | 1 |
| 2. The speakers covered the material sufficiently. | 5 | 4 ✓ | 3 | 2 | 1 |
| 3. The information presented was relevant to your needs and expectations. | 5 | 4 | 3 ✓ | 2 | 1 |
| 4. Sufficient opportunity was provided for questions. | 5 | 4 ✓ | 3 | 2 | 1 |
| 5. You were pleased with the presentations. | 5 | 4 | 3 | 2 ✓ | 1 |

[Signature]
Signature



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

FEEDBACK

Name of the Participant: B. Thirupathi

Designation: Asst. Prof

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

1. The speakers presented materials clearly and concisely.

5 4 3 2 1

2. The speakers covered the material sufficiently.

5 4 3 2 1

3. The information presented was relevant to your needs and expectations.

5 4 3 2 1

4. Sufficient opportunity was provided for questions.

5 4 3 2 1

5. You were pleased with the presentations.

5 4 3 2 1

mflw
Signature



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

FEEDBACK

Name of the Participant: M. Kranthi Kumar

Designation: Asst. Prof

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

1. The speakers presented materials clearly and concisely.

5 4 3 2 1

2. The speakers covered the material sufficiently.

5 4 3 2 1

3. The information presented was relevant to your needs and expectations.

5 4 3 2 1

4. Sufficient opportunity was provided for questions.

5 4 3 2 1

5. You were pleased with the presentations.

5 4 3 2 1

Handwritten Signature
Signature



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

FEEDBACK

Name of the Participant: B. Pooja

Designation: Asst. Prof

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

1. The speakers presented materials clearly and concisely.

5 4 3 2 1

2. The speakers covered the material sufficiently.

5 4 3 2 1

3. The information presented was relevant to your needs and expectations.

5 4 3 2 1

4. Sufficient opportunity was provided for questions.

5 4 3 2 1

5. You were pleased with the presentations.

5 4 3 2 1

Pooja
Signature



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

FEEDBACK

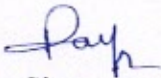
Name of the Participant: Gr. Pratheepa

Designation: Asst - prof

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

- | | | | | | |
|---|---|-----|-----|-----|---|
| 1. The speakers presented materials clearly and concisely. | 5 | 4 | 3 ✓ | 2 | 1 |
| 2. The speakers covered the material sufficiently. | 5 | 4 ✓ | 3 | 2 | 1 |
| 3. The information presented was relevant to your needs and expectations. | 5 | 4 | 3 ✓ | 2 | 1 |
| 4. Sufficient opportunity was provided for questions. | 5 | 4 ✓ | 3 | 2 | 1 |
| 5. You were pleased with the presentations. | 5 | 4 | 3 | 2 ✓ | 1 |


Signature



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

FEEDBACK

Name of the Participant: L. Prakash

Designation: Asst. Prof.

Please respond to the following items by using the scale below:

5=Excellent 4=Very Good 3=Good 2=Fair 1=Poor

1. The speakers presented materials clearly and concisely.

5 4 3 2 1

2. The speakers covered the material sufficiently.

5 4 3 2 1

3. The information presented was relevant to your needs and expectations.

5 4 3 2 1

4. Sufficient opportunity was provided for questions.

5 4 3 2 1

5. You were pleased with the presentations.

5 4 3 2 1


Signature



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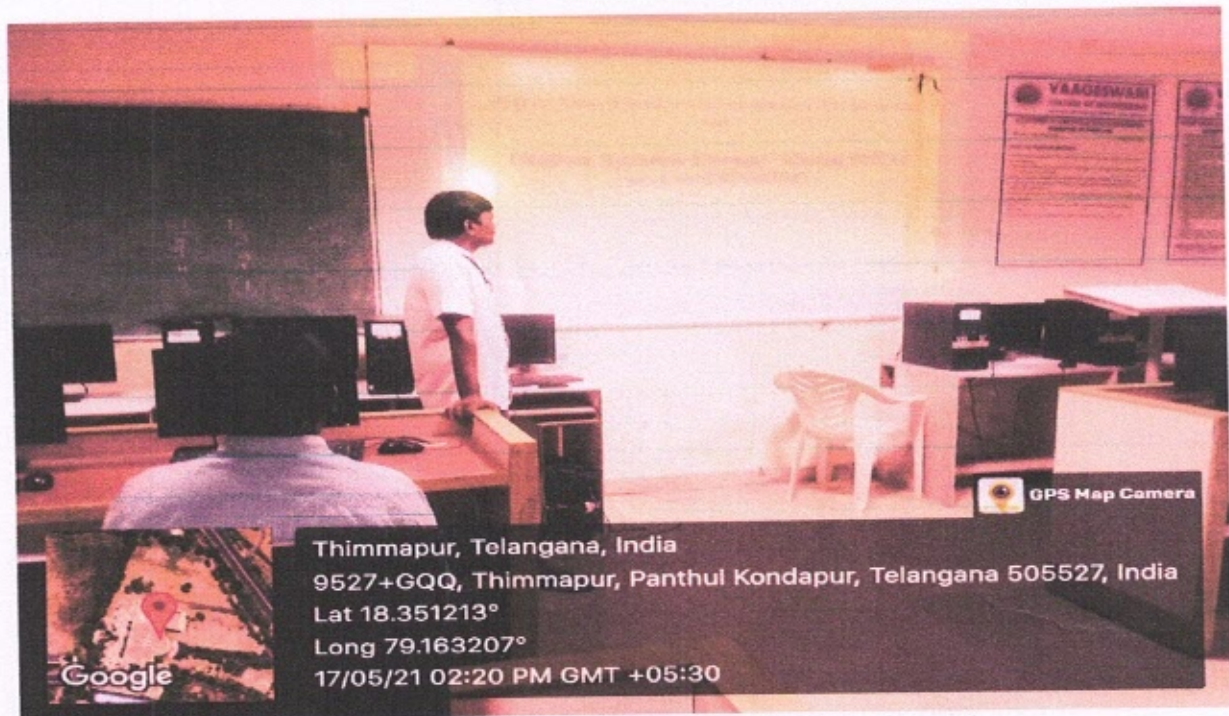
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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)



Mr.B.Bhargavendra, Associate Professor, Department of ECE addressing the gathering of the
Faculty Development Program

Principal

Vaageswari College of Engineering

KARIMNAGAR-505 527.

Beside L.M.D. Police Station, KARIMNAGAR - 505 527, Telangana State. Ph : 0878-2004242

E-mail: s4.principal@gmail.com, Website: www.vgsek.ac.in



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VAAGESWARI COLLEGE OF ENGINEERING

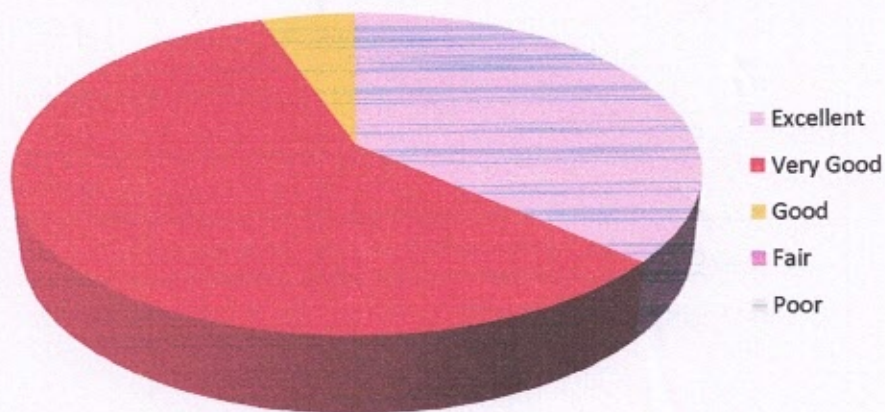
(Affiliated to JNTUH, Hyderabad.)

(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

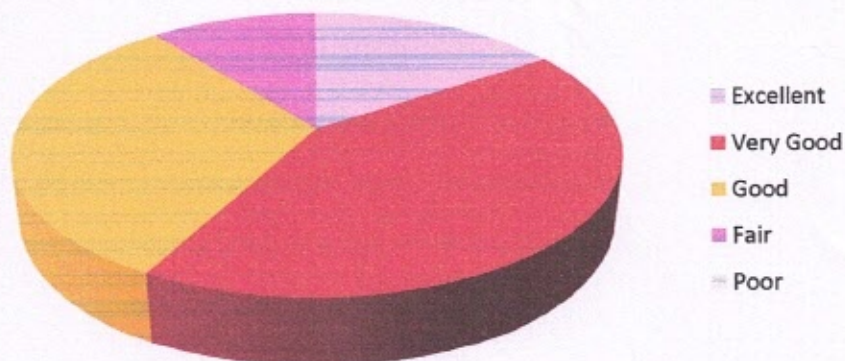
Department of Electronics and Communication Engineering
Five-Day Faculty Development Program on
DIGITAL SYSTEM DESIGN USING DSCH AND MICROWIND
(17/05/2021 to 21/05/2021)

ANALYSIS

Speakers Presented Materials Clearly



Sufficient Opportunity was provided for questions



Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.

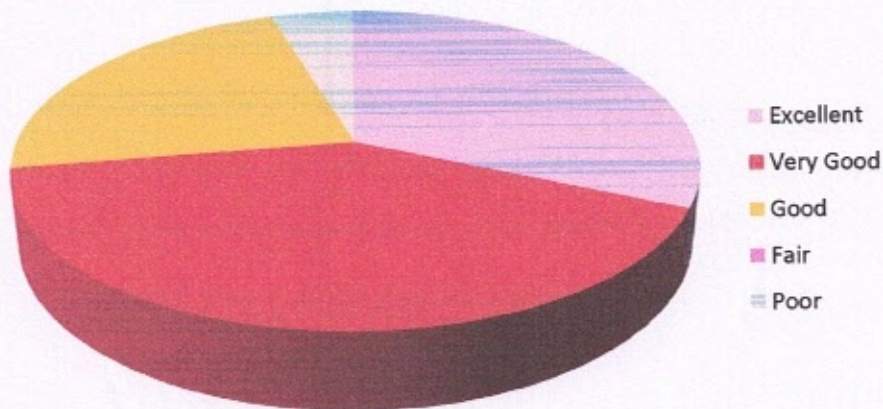


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Were you pleased with the program



ACTION TAKEN

The organizing committee has taken a decision to conduct the upcoming Faculty Development Programmes in a very effective and efficient manner. The committee also decided to invite speakers from very reputed organizations. It has been decided to make the programmes more of practical oriented and more focus is to be put on latest and emerging technologies which would make the staff members to be aware of the latest trending methods to deal with real time applications. The committee has decided to conduct programs on Front end and Back end VLSI Design.

Principal

Vaageswari College of Engineering
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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Digital System Design Using DSCH and Microwind

(17/05/2021 to 21/05/2021)

REPORT

A five Day Faculty Development Program on “**Digital System Design using DSCH and Microwind**” was organized by Department of Electronics and Communication Engineering, Vaageswari college of Engineering from 17th to 21st May 2021 for the faculty of ECE Department. Several faculty members of the Department actively participated and gained useful information from all the sessions.


The program is designed to design and develop Digital circuits at Schematic and Layout levels. The program provided an opportunity to acquire knowledge about current technological developments in the field of Digital Design. The program mainly focused on conceptual Digital Design and with primary focus on designing digital circuits and layouts in 180 nm technology.

Outcomes of the Faculty Development Program:

1. The participants gained hands-on experience of how to work with circuit simulator DSCH and Layout editor Microwind.
2. The participants also gained knowledge on how a MOS transistor works and aspects of regions of operation.
3. The participants gained experience in sizing complex combinational logic digital circuits and sequential circuits.


Coordinator


HOD


Principal
Principal
Vaageswari College of Engineering
KARIMNAGAR-505 527.



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Date: 31/08/2021

To
The Principal,
Vaageswari College of Engineering,
Karimnagar.


Subject: Request Proposal to Conduct Five day FDP on “VLSI DESIGN
USING FPGA”-Reg.

Respected Sir,

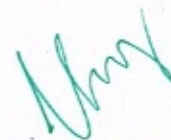
In Accordance to Subject cited above I, Head of the Department of Electronics and Communication Engineering here with request with this letter to permit us to conduct a Five day Faculty Development Program on “VLSI DESIGN USING FPGA” during 13-09-2021 to 17-09-2021. I also request you to provide all the required facilities.

Thanking You,

Yours Sincerely


HOD-Dept. of ECE.

Permitted



Principal
Vaageswari College of Engineering
KARIMNAGAR-505 527.



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(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

Date: 03-09-2021


CIRCULAR

It is hereby informed to Department of Electronics and Communication Engineering, that a Five day Faculty Development Program will be held on "VLSI DESIGN USING FPGA" from 13th September to 17th September 2021. All the teaching and non-teaching members should avail this opportunity to upgrade the technical skills.

Hence, all teaching and non-teaching members should attend the above FDP program without fail.

Copies to:

- To circulate among all the staff members
- All the Notice Boards
- To all HODS
- Office file


Principal
Vaageswari College of Engineering
KARIMNAGAR-505 527.

FDP CONTENT:

Primary objective of this program is to provide hands on experience in Digital VLSI using FPGA board. During this program our focus will be on practical realization of digital circuits using Vivado and become aware of current state-of-art in industry in this domain. The following are the broad topics, going to be covered in this course:

Basic of hardware description languages Verilog/VHDL and FPGA based realization; Industries based embedded system designing, Advancements in ASIC designing.

One of the key features of this program is that, along with discussing the advanced research topics and cutting edge technologies, it will encompass fundamental aspects of digital as well as Electrical and Electronics domain technologies.

PROGRAMME CONTENT:

- Knowledge and understanding of fundamental recent trends in FPGA technologies and identifying and understanding open problems and challenges.
- Preliminary ideal for using hardware description languages Verilog/VHDL and FPGA based realization as per today's industry needs.
- Basic awareness of ASIC design, full custom design flow, spice net list, spice models and analysis, layer map file, DRC and LVS rule layout design.

ADVISORY COMMITTEE

CHIEF PATRON

Dr. G. Sreenivas Reddy
General Secretary and Correspondent

PATRON

Sri. D. Srinivas Reddy, Joint-Secretary
Dr. Ch. Srinivas, Principal

COORDINATOR

Mr. B. THIRUPATHI.

Associate Professor,
Department of ECE

ORGANIZING COMMITTEE

Mr. G. S. ARUN KUMAR

Mr. M. KRANTHI KUMAR

Mr. LAXMI NARAYANA

Mr. T. NAGESWAR RAO

Mr. CH. PRANAY RAJ

CONVENER

B. Bhargavendra

Associate Professor,

**Department of Electronics & Communication
Engineering**

Email: s4.ecehod@gmail.com

Phone: +91-9949301126



Principal

Vaageswari College of Engineering

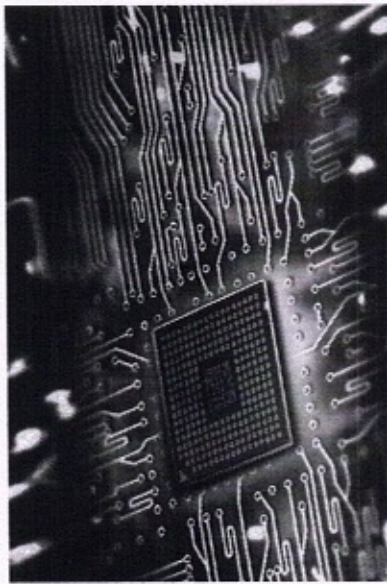


**FIVE-DAY FACULTY
DEVELOPMENT PROGRAM**

ON

VLSI DESIGN USING FPGA

13th to 17th, September 2021



Organized

By

**Department of Electronics & Communications
Engineering
VAAGESWARI COLLEGE OF
ENGINEERING**

(APPROVED BY AICTE, AFFILIATED TO JNTUH)
RAMAKRISHNA COLONY, KARIMNAGAR
www.vgsek.ac.in

ABOUT THE INSTITUTE:

Established in 2003, is one of the most prestigious society providing high quality professional education in Karimnagar district Telangana. The society is running multiple educational institutions like Vaageswari College of Engineering (2005), Pharmacy (2003), Pharmaceutical Sciences (2007), Management Sciences (2008). The society was established with a great vision to provide quality education on par with global standards to the students from all over India in general and in particular those from local and rural areas. Society is maintaining high standards so as to make them technically competent and ethically strong individuals who shall be able to improve the quality of life and become an asset to the economy of our country. The Society not only celebrates freedom of thought, cultivates vision and encourages growth, but also inculcates human values and concern for the environment and the society. The institution is awarded with Best Chapter of ISTE AP Section.

VISION:

"To develop the SreeVaageswari Educational Society into a center of excellence in imparting professional and technical education, to inculcate knowledge and power to the younger generation by providing high quality professional education on par with global standards and to instill high levels of discipline among student community making them ethically strong citizens capable of contributing to the development of society and mankind".

PROGRAMS OFFERED:

The college offers B.Tech. programmes in CSE, EEE, ECE, Civil and Mechanical Engineering. The college also offers MCA, MBA and M.Tech. in Computer Science and Engineering.

ABOUT THE DEPARTMENT:

The department of Electronics and Communication Engineering (ECE) was started in 2005 with an intake of 60 and in a short span of time the intake increased to 180. The department has got M.Tech in ECE with an intake of 36 during this academic year (2010-11)

The department comprises of well experienced and dedicated faculty with vast experience in teaching. The faculty members have been meticulously and judiciously picked up to impart quality education and inculcate all-round development in the Students. The department has well established labs with state of art hardware equipments and advanced softwares.

The department is run by well qualified and experienced faculty under the able guidance and leadership of Mr. R.Naraiah, HOD, ECE.

Consequently, the administration is trying to play the role of resources and technical leadership in development activities at the national level.

The department also seeks to provide its students with excellent educational and educational opportunities in the field of electronics and communications engineering, thus providing a lasting technical basis in an environment of rapid technical change. In the process, it enables and promotes their professional growth through contact with best professional practices. Consequently, the administration is trying to play the role of resources and technical leadership in development activities at the national level.



Principal

Vaageswari College of Engineering

REGISTRATION FORM:

Please fill this form and send it to us by email to s4.ecehod@gmail.com before 09th September 2021

Name of the Participant: _____

Designation: _____

Department: _____

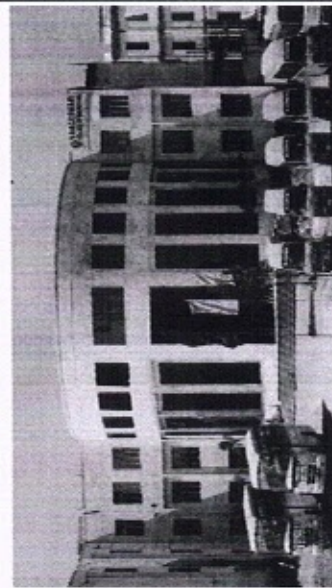
Organization name and Address: _____

Mobile Number: _____

Email id: _____

Signature and Seal of the
Head /Director of the Institute

NO REGISTRATION FEE





SREE VAAGESWARI EDUCATIONAL SOCIETY
VAAGESWARI COLLEGE OF ENGINEERING

(Affiliated to JNTUH, Hyderabad.)

(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

INVITATION LETTER

To,

Dr. Pankaj Hivraj Rangaree,

Assistant Professor,

Department of Electronics and Communication Engineering,

Vaagdevi College of Engineering,

Warangal-506005.

Sub: Invitation to be a resource person for five day Faculty Development Program on “VLSI DESIGN USING FPGA”-regarding.

Dear Sir,

On behalf of Vaageswari College of Engineering, I cordially invite you to deliver a lecture on **VLSI DESIGN USING FPGA** which will be held from 13th to 17th September 2021. I would be thankful if you share your expertise on the topic with our faculty.

I will be eagerly looking forward to hear from you and hope that you would accept our invitation for the event.

Accepted and Acknowledged
[Signature]

Dr. Ch. Srinivas,

Principal,

Vaageswari College of Engineering,

Karimnagar.

Email: s4.principal@gmail.com

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

Speaker Profiles

Dr. Korlapati Keerti Kumar, completed his Ph.D from National Institute of Technology, Warangal in the year 2016. Currently he is working as an Associate Professor at Vaageswari College of Engineering. His research interests include Microelectronics, Digital Design and Device Modeling.

Mr. B. Bhargavendra, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Associate Professor at Vaageswari College of Engineering. His research interests include CMOS Design.

Mr. K.Vijay Kumar, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Assistant Professor at Vaageswari College of Engineering. His research interests include RF IC Design and Device Modeling

Mr. P.Ajay Kumar, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Assistant Professor at Vaageswari College of Engineering. His research interests include VLSI Design and MEMS.

Principal
Vaageswari College of Engineering
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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

SCHEDULE

Date	Morning Session (10.00 AM -12.30 AM)	Afternoon Session (1.30PM-4.00 PM)	Resource Person
Day 1 13-09-2021	Inauguration	Recent trends in FPGA technologies	Dr. K.Keerti Kumar Associate Professor, Department of ECE. Vaageswari College of Engineering
Day 2 14-09-2021	Realization of Digital Circuits	Realization of digital circuits using Vivado	Mr.B.Bhargavendra Associate Professor, Department of ECE. Vaageswari College of Engineering
Day 3 15-09-2021	Preliminary ideal for using hardware description languages Verilog/VHDL	Industries based embedded System designing	Mr.K.Vijay Kumar, Assistant. Professor, Department of ECE. Vaageswari College of Engineering
Day 4 16-09-2021	Advancements in ASIC designing	Full custom design flow	Mr.P.Ajay Kumar, Assistant. Professor, Department of ECE. Vaageswari College of Engineering.
Day 5 17-09-2021	Spice net list, spice models and analysis, layer map file, DRC and LVS rule layout design.	Feedback and Valedictory	Dr. K.Keerti Kumar Associate Professor, Department of ECE. Vaageswari College of Engineering.


Coordinator


HOD


Principal
Principal

Vaageswari College of Engineering,
KARIMNAGAR-505 527.



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

LIST OF PARTICIPANTS

S.No.	Name of the Participant	Signature
1	E.JYOTHI	
2	THIRUPATHI BAJJURI	
3	B. RAM MOHAN	
4	KALAKONDA JYOTHSNA	
5	Dr.K.KEERTI KUMAR	
6	B.BHARGAVENDRA	
7	K.VIJAY KUMAR	
8	P.AJAY KUMAR	
9	L PRAKASH	
10	M.KRANTHI KUMAR	
11	G.S.ARUN KUMAR	
12	ARUN KUMAR AKULA	
13	A.VENKATA REDDY	
14	NAGESWARRAO TEKULAPALLY	
15	DEEKONDA LAXMI NARAYANA	

Coordinator

HOD

Principal
Principal
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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

ATTENDANCE SHEET

S.No.	Name of the Participant	13/09/2021	14/09/2021	15/09/2021	16/09/2021	17/09/2021
1	E.JYOTHI					
2	THIRUPATHI BAJJURI					
3	B. RAM MOHAN					
4	KALAKONDA JYOTHSNA					
5	Dr.K.KEERTI KUMAR					
6	B.BHARGAVENDRA					
7	K.VIJAY KUMAR					
8	P.AJAY KUMAR					
9	L PRAKASH					
10	M.KRANTHI KUMAR					
11	G.S.ARUN KUMAR					
12	ARUN KUMAR AKULA					
13	A.VENKATA REDDY					
14	NAGESWARRAO TEKULAPALLY					
15	DEEKONDA LAXMI NARAYANA					

Coordinator

HOD

Principal
Principal
Vaageswari College of Engineering
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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: E. S. Sathish

2. Designation & Department

: Assoc. Prof. & ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills			✓		
3	Self Confidence				✓	
4	Conducting class room discussions					✓
5	Reference to the latest development			✓		
6	Linking subject to the life experience				✓	
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking			✓		
9	Encourages me to raise and make comments				✓	
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject			✓		
3	Ability to clear areas of confusion					✓
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning					✓
7	Contributes to improve my learning				✓	
8	Quality of study material provided			✓		
9	Quality of good reference provided				✓	
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants				✓	
2	Respecting opinion and decision of participants			✓		
3	Willingness to learn from the participants				✓	
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5


Signature



SREE VAAGESWARI EDUCATIONAL SOCIETY VAAGESWARI COLLEGE OF ENGINEERING

(Affiliated to JNTUH, Hyderabad.)

(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: B. Thirupathi

2. Designation & Department

: Asst. Prof., ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills				✓	
3	Self Confidence					
4	Conducting class room discussions			✓		
5	Reference to the latest development				✓	
6	Linking subject to the life experience					✓
7	Creating interest in the subject			✓		
8	Encouraging high order of critical thinking		✓			
9	Encourages me to raise and make comments			✓		
10	Challenges me to think				✓	
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class					✓
2	Knows his/her subject				✓	
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs					✓
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning			✓		
8	Quality of study material provided		✓			
9	Quality of good reference provided			✓		
10	Time management in the class				✓	
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5

mfw
Signature



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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: B. Ramkhan.

2. Designation & Department

: Asst. prof ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills			✓		
3	Self Confidence					✓
4	Conducting class room discussions				✓	
5	Reference to the latest development			✓		
6	Linking subject to the life experience					✓
7	Creating interest in the subject			✓		
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments			✓		
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject			✓		
3	Ability to clear areas of confusion					✓
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning					✓
8	Quality of study material provided			✓		
9	Quality of good reference provided				✓	
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

Dany
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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: K. Jyothsna

2. Designation & Department

: Asst-Prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills				✓	
3	Self Confidence					✓
4	Conducting class room discussions			✓		
5	Reference to the latest development					✓
6	Linking subject to the life experience			✓		
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking			✓		
9	Encourages me to raise and make comments					✓
10	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject					✓
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept					✓
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning					✓
7	Contributes to improve my learning					✓
8	Quality of study material provided			✓		
9	Quality of good reference provided					✓
10	Time management in the class				✓	
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class					✓
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA (13/09/2021 to 17/09/2021)

1. Name of the Participant : Dr. K. Keerthi Kumar

2. Designation & Department : Professor, ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills				✓	
3	Self Confidence			✓		
4	Conducting class room discussions		✓			
5	Reference to the latest development			✓		
6	Linking subject to the life experience				✓	
7	Creating interest in the subject			✓		
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments		✓			
10	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class					✓
2	Knows his/her subject				✓	
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept		✓		✓	
5	Flexibility in accommodating individual participants needs					✓
6	Uses effective teaching method to enhance learning			✓		✓
7	Contributes to improve my learning				✓	
8	Quality of study material provided		✓			
9	Quality of good reference provided				✓	
10	Time management in the class					✓
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants				✓	
4	Punctuality in class			✓		
S. No.	D.) Overall Performance	1	2	3	4	5

KKK
Signature



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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: B. Bhargavendra

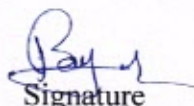
2. Designation & Department

: Assoc. Prof & ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic			✓		
2	Communication skills				✓	
3	Self Confidence					✓
4	Conducting class room discussions				✓	
5	Reference to the latest development			✓		
6	Linking subject to the life experience				✓	
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments			✓		
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class			✓		
2	Knows his/her subject				✓	
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning				✓	✓
7	Contributes to improve my learning				✓	
8	Quality of study material provided			✓		
9	Quality of good reference provided				✓	
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5


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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: K. Vijay Kumar

2. Designation & Department

: Asst. Prof. & ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills			✓		
3	Self Confidence					✓
4	Conducting class room discussions				✓	
5	Reference to the latest development			✓		
6	Linking subject to the life experience				✓	
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments			✓		
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject			✓		
3	Ability to clear areas of confusion				✓	
4	Ability to clearly explains concept					✓
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning					✓
8	Quality of study material provided				✓	
9	Quality of good reference provided			✓		
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants			✓		
3	Willingness to learn from the participants				✓	
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5


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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant : P. Ajay Kumar

2. Designation & Department : Asst Prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills	✓				
3	Self Confidence				✓	
4	Conducting class room discussions			✓		
5	Reference to the latest development				✓	
6	Linking subject to the life experience					✓
7	Creating interest in the subject				✓	
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments					✓
10	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject					✓
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept					✓
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning					✓
7	Contributes to improve my learning		✓			
8	Quality of study material provided					✓
9	Quality of good reference provided		✓			
10	Time management in the class					✓
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants				✓	
2	Respecting opinion and decision of participants					✓
3	Willingness to learn from the participants			✓		
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

P. Ajay Kumar
Signature



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: L. Prakash

2. Designation & Department

: Asst. Prof & ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills			✓		
3	Self Confidence					✓
4	Conducting class room discussions					✓
5	Reference to the latest development				✓	
6	Linking subject to the life experience			✓		
7	Creating interest in the subject		✓			
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments					✓
10	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class					✓
2	Knows his/her subject					✓
3	Ability to clear areas of confusion				✓	
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs				✓	
6	Uses effective teaching method to enhance learning		✓			
7	Contributes to improve my learning				✓	
8	Quality of study material provided					✓
9	Quality of good reference provided			✓		
10	Time management in the class				✓	
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants		✓			
4	Punctuality in class					✓
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA (13/09/2021 to 17/09/2021)

1. Name of the Participant : M. KRANTHI KUMAR

2. Designation & Department : Asst. prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills				✓	
3	Self Confidence			✓		
4	Conducting class room discussions		✓			
5	Reference to the latest development			✓		
6	Linking subject to the life experience				✓	
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments			✓		
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject					✓
3	Ability to clear areas of confusion				✓	
4	Ability to clearly explains concept			✓		
5	Flexibility in accommodating individual participants needs		✓			
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning					✓
8	Quality of study material provided				✓	
9	Quality of good reference provided			✓		
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: G.S. Arun Kumar

2. Designation & Department

: Asst- prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills			✓		
3	Self Confidence					✓
4	Conducting class room discussions			✓		
5	Reference to the latest development					✓
6	Linking subject to the life experience		✓			
7	Creating interest in the subject			✓		
8	Encouraging high order of critical thinking					✓
9	Encourages me to raise and make comments			✓		
10	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject					✓
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept					✓
5	Flexibility in accommodating individual participants needs					✓
6	Uses effective teaching method to enhance learning			✓		
7	Contributes to improve my learning					✓
8	Quality of study material provided				✓	
9	Quality of good reference provided					✓
10	Time management in the class					✓
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants					✓
4	Punctuality in class					✓
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: A. Avunkumar

2. Designation & Department

: Asst. prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills					✓
3	Self Confidence					✓
4	Conducting class room discussions					✓
5	Reference to the latest development		✓			
6	Linking subject to the life experience				✓	
7	Creating interest in the subject			✓		
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments			✓		
10	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class		✓			
2	Knows his/her subject				✓	
3	Ability to clear areas of confusion				✓	
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning					✓
7	Contributes to improve my learning					✓
8	Quality of study material provided			✓		
9	Quality of good reference provided				✓	
10	Time management in the class					✓
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants		✓			
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants				✓	
4	Punctuality in class					✓
S. No.	D.) Overall Performance	1	2	3	4	5

Signature



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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant

: A. Venkata Reddy

2. Designation & Department

: ASSOC. PROF, ECE

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills					✓
3	Self Confidence			✓		
4	Conducting class room discussions					✓
5	Reference to the latest development			✓		
6	Linking subject to the life experience					✓
7	Creating interest in the subject			✓		
8	Encouraging high order of critical thinking					✓
9	Encourages me to raise and make comments			✓		
10	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class			✓		
2	Knows his/her subject					✓
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept					✓
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning		✓			
7	Contributes to improve my learning					✓
8	Quality of study material provided			✓		
9	Quality of good reference provided					✓
10	Time management in the class			✓		
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants		✓			
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants					✓
4	Punctuality in class			✓		
S. No.	D.) Overall Performance	1	2	3	4	5

Signature



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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA (13/09/2021 to 17/09/2021)

1. Name of the Participant : T. Nageshwar Rao
2. Designation & Department : Assoc Prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills					✓
3	Self Confidence			✓		
4	Conducting class room discussions				✓	
5	Reference to the latest development					✓
6	Linking subject to the life experience			✓		
7	Creating interest in the subject				✓	
8	Encouraging high order of critical thinking			✓		
9	Encourages me to raise and make comments		✓			
10	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class					✓
2	Knows his/her subject				✓	
3	Ability to clear areas of confusion					
4	Ability to clearly explains concept			✓		
5	Flexibility in accommodating individual participants needs		✓			
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning					✓
8	Quality of study material provided				✓	
9	Quality of good reference provided			✓		
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5


Signature



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Department of Electronics and Communication Engineering Five-Day Faculty Development Program on VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

1. Name of the Participant : D. Laxminarayana

2. Designation & Department : Asst. prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills			✓		
3	Self Confidence					✓
4	Conducting class room discussions					✓
5	Reference to the latest development					✓
6	Linking subject to the life experience				✓	
7	Creating interest in the subject			✓		
8	Encouraging high order of critical thinking		✓			
9	Encourages me to raise and make comments				✓	
10	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject		✓			
3	Ability to clear areas of confusion			✓		
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs					✓
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning				✓	
8	Quality of study material provided					✓
9	Quality of good reference provided			✓		
10	Time management in the class				✓	
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants					✓
3	Willingness to learn from the participants			✓		
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5

Signature



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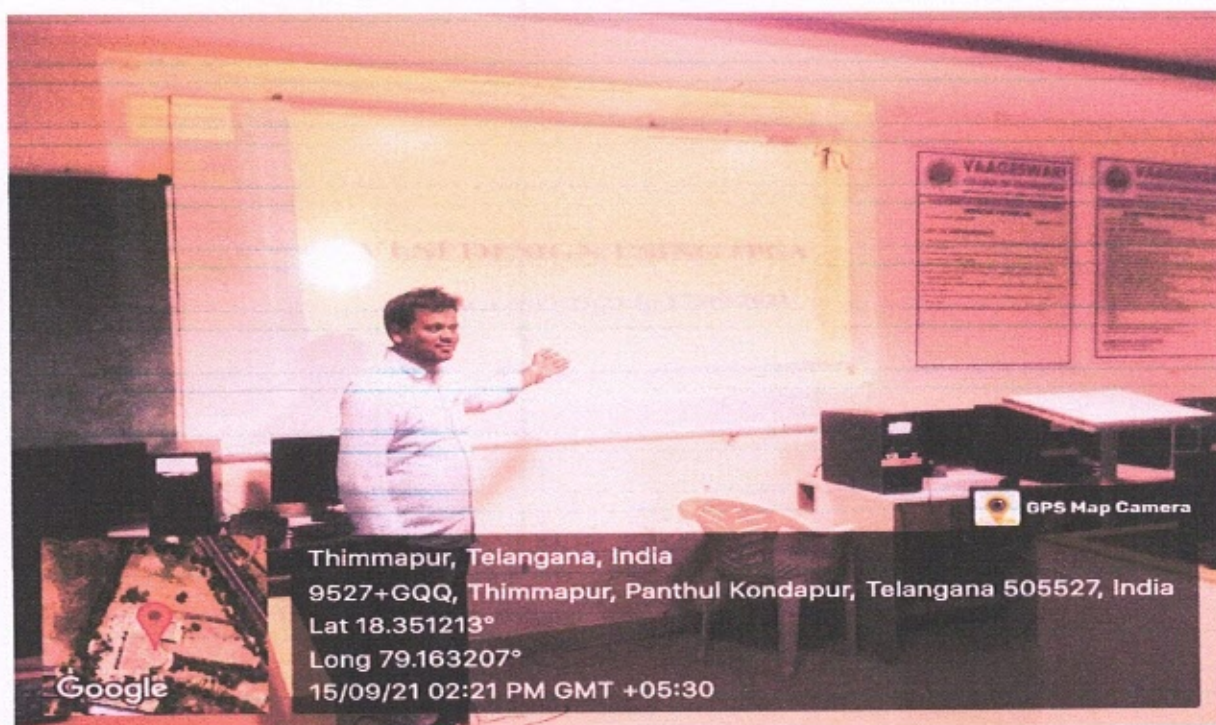
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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)



Mr.D.Laxminarayana, Assistant Professor, Department of ECE giving a brief introduction about research areas in ECE.


Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527

Beside L.M.D. Police Station, KARIMNAGAR-505 527, Telangana State. Ph : 0878-2004242
E-mail: s4.principal@gmail.com, Website: www.vgsek.ac.in



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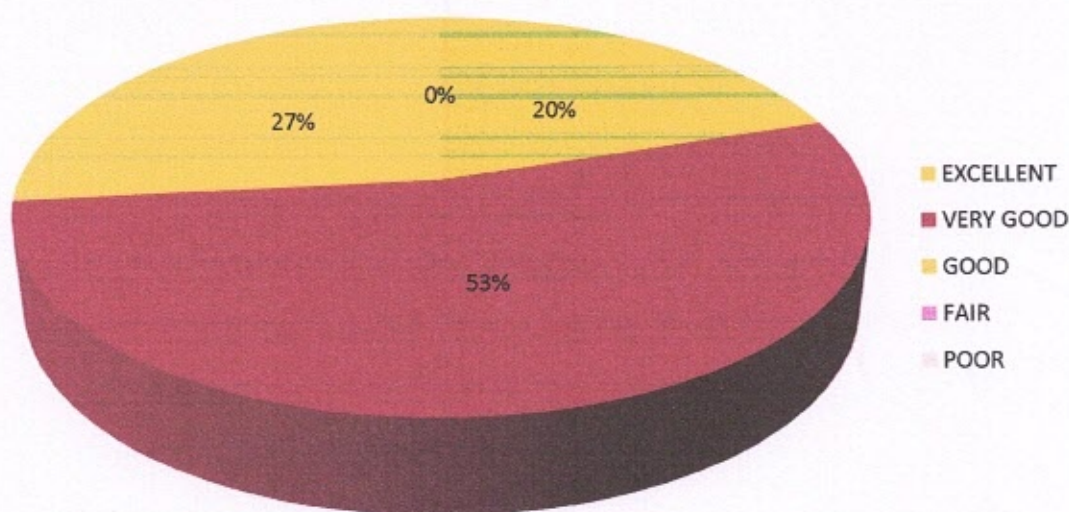
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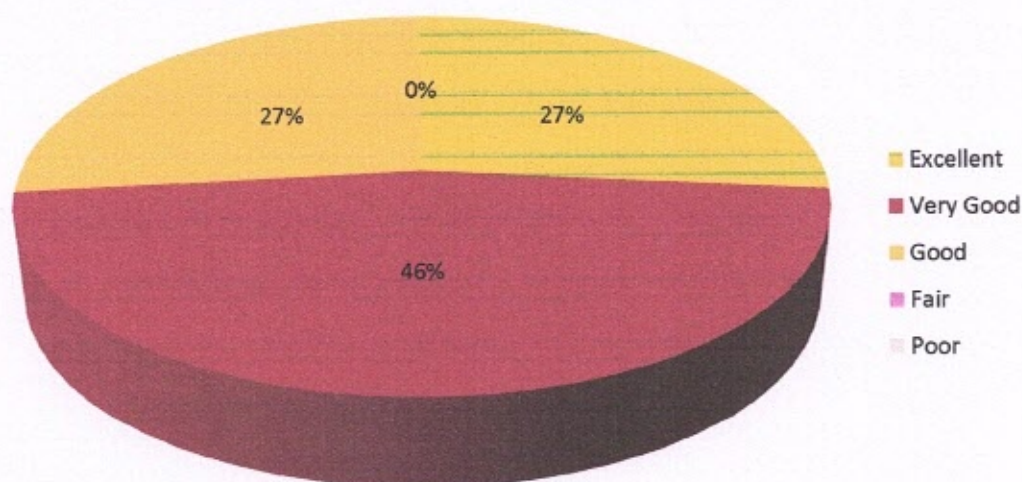
Department of Electronics and Communication Engineering
Five-Day Faculty Development Program on
VLSI DESIGN USING FPGA
(13/09/2021 to 17/09/2021)

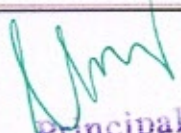
ANALYSIS

Linking subject to the life experience



Ability to clearly explains concept



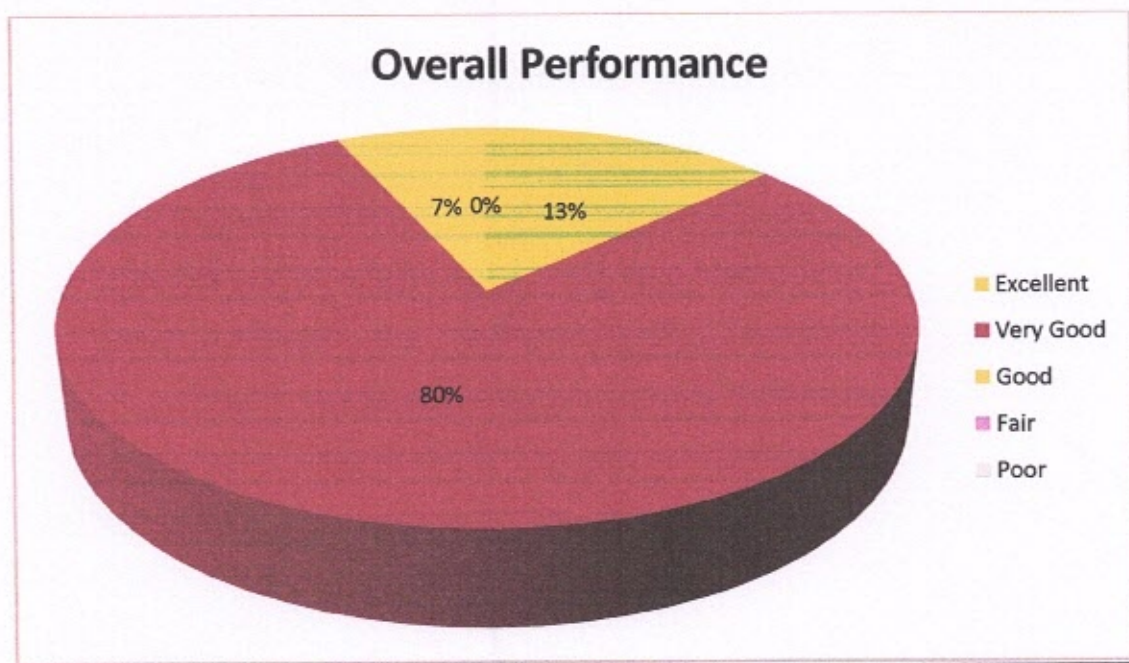

Principal
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ACTION TAKEN

The organizing committee has taken a decision to conduct the upcoming Faculty Development Programmes in a very effective and efficient manner. The committee also decided to invite speakers from very reputed organizations. It has been decided to make the programmes more of practical oriented and more focus is to be put on latest and emerging techniques related to VLSI. The committee decided to organize more programmes on VLSI Physical Design and Testing.

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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

VLSI DESIGN USING FPGA

(13/09/2021 to 17/09/2021)

Report

Five Day Faculty Development Programme on “VLSI DESIGN USING FPGA” was organized by Department of Electronics and Communication Engineering, Vaageswari college of Engineering from 13th to 17th September 2021 for the faculty of ECE Department. Around 20 faculty members of the Department actively participated and gained useful information from all the sessions.

Mr.P.Ajay Kumar, Assistant Professor, ECE Department, Vaageswari college of Engineering conducted FDP on VLSI Design using FPGA. It was initiated with the introduction to VIVADO.

The Electronics industry has achieved a phenomenal growth over the last few decades, mainly due to the rapid advances in large scale integration technologies and system design applications. With the advent of very large scale integration (VLSI) designs, the number of applications of integrated circuits (ics) in high-performance computing, controls, telecommunications, image and video processing, and consumer electronics has been rising at a very fast pace.

Outcomes of the Faculty Development Programme:

- FPGA technologies
- Preliminary ideal for using hardware description languages Verilog/VHDL and FPGA based realization as per today's industry needs.
- Basic awareness of ASIC design
- Advancements in ASIC designing

TECHNICAL SESSIONS:

1st Day (13-09-2021)


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This FDP is intended to make the participants aware and able to work on this emerging area. Around 20 participants attended this FDP to gain knowledge in the area of designing projects and applications by the use of VLSI Design using FPGA.

During the day Mr. K.KEERTI KUMAR, Associate Professor, ECE Department introduced the audience about Recent trends in FPGA technologies

2nd Day (14-09-2021)

Mr.B.BHARGAVENDRA Associate Professor, ECE Department introduced the Participants about Realization of digital circuits and Realization of digital circuits using Vivado. The participants interacted with resource person and discussed about implementation problems they are facing in their research

3rd Day (15-09-2021)

Mr.K.Vijay Kumar, Assistant. Professor, Department of ECE, had covered the topic Preliminary ideal for using hardware description languages Verilog/VHDL and introduced Industries based embedded system designing

4th Day (16-09-2021)

Mr.K.Gopi Krishna, Assistant. Professor, Department of ECE, had covered the topic Advancements in ASIC designing and Full custom design flow. The participants interacted with the resource person and their doubts on System design.

5th Day (17-09-2021)

Mr. K.KEERTI KUMAR, Associate Professor, ECE Department had, covered the topic Spice net list, spice models and analysis, layer map file, DRC and LVS rule layout design

Co-ordinator

HOD

PRINCIPAL
Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



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(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

Date: 21/03/2022

To
The Principal,
Vaageswari College of Engineering,
Karimnagar.

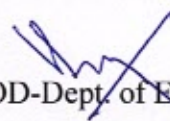
Subject: Request Proposal to Conduct Five day FDP on “Recent Trends in Microwave and Antenna Technology”-Reg.

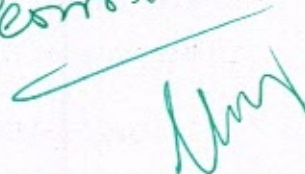
Respected Sir,

In Accordance to Subject cited above I, Head of the Department of Electronics and Communication Engineering here with request with this letter to permit us to conduct a Five day Faculty Development Program on “Recent Trends in Microwave and Antenna Technology” during **04-04-2022 to 08-04-2022**. I also request you to provide all the required facilities.

Thanking You,

Yours Sincerely


HOD-Dept. of ECE.

Permitted


Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



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(Affiliated to JNTUH, Hyderabad.)

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Date: 23-03-2022

CIRCULAR

It is hereby informed to Department of Electronics and Communication Engineering that a three day Faculty Development Program will be held on "**Recent Trends in Microwave and Antenna Technology**" from 04th to 08th April 2022. All the teaching and non-teaching members should avail this opportunity to upgrade the technical skills.

Hence, all teaching and non-teaching members should attend the above FDP program without fail.

Copies to:

- To circulate among all the staff members
- All the Notice Boards
- To all HODS
- Office file

PRINCIPAL

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.

FDP CONTENT:

The program intends to present the methodology for the design and development of the components at Microwave and Millimeter Wave Frequencies. It attempts to provide a timely update on latest trends, technologies and tools in RF and Microwave domain. The curriculum will present the techniques for design, modelling and fabrication of different types of basic antennas. It aims to provide a platform to the participant to grab knowledge in the field of Applied Electromagnetics in various applications and about the emerging technology of Microwave integrated circuits. The program will include the various aspects of microstrip antennas and the state of the art developments that have taken place in the recent time. It opens up opportunity to highlight the design consideration and application of the reconfigurable antennas in the realm of wireless communication. The course will address the issues like EMI/EMC and the measurement of the radiated electromagnetic field quantities to ensure the satisfactory performances of the electrical or electronic equipment's in Electromagnetic Environment.

- Microwave devices
- Microwave circuits and Metamaterials
- Millimeter waves
- sub millimeter-waves
- technologies Micro strip antennas
- Smart Antenna and MIMO antenna
- Antenna arrays, reconfigurable antennas
- Radar, imaging, sensing and localization
- Ultra-wideband techniques
- Applications EMI/EMC

ADVISORY COMMITTEE

CHIEF PATRON

Dr. G. Sreenivas Reddy
General Secretary and Correspondent

PATRON

Sri. D. Srinivas Reddy, Joint-Secretary
Dr. Ch. Srinivas, Principal

COORDINATOR

Mrs. E.Jyothi
Associate Professor,
Department of ECE

ORGANIZING COMMITTEE

Mr. K.Vijay Kumar
Mrs.K.Jyothsna
Ms.K.Pravalika
Mr.P.Ajay Kumar
Mr. CH.Pranay Raj
Mr. L.Prakash

CONVENER

Mr. T.Nageshwar Rao
Assistant Professor

**Department of Electronics and
Communications Engineering**

Email: s4.ecehod@gmail.com
Phone: +91-9949521356



FIVE-DAY FACULTY DEVELOPMENT PROGRAM

ON

**Recent Trends in Microwave
and Antenna Technology
04th to 08th, April 2022**



Organized

By

**Department of Electronics & Communications
Engineering
VAAGESWARI COLLEGE OF
ENGINEERING
(APPROVED BY AICTE, AFFILIATED TO JNTUHH)
RAMAKRISHNA COLONY, KARIMNAGAR
www.vgsek.ac.in**

Principal
Vaageswari College of Engineering
KARIMNAGAR-505 527

ABOUT THE INSTITUTE:

Established in 2003, is one of the most prestigious society providing high quality professional education in Karimnagar district Telangana. The society is running multiple educational institutions like Vaageswari College of Engineering (2005), Pharmacy (2003), Pharmaceutical Sciences (2007), Management Sciences (2008). The society was established with a great vision to provide quality education on par with global standards to the students from all over India in general and in particular those from local and rural areas. Society is maintaining high standards so as to make them technically competent and ethically strong individuals who shall be able to improve the quality of life and become an asset to the economy of our country. The Society not only celebrates freedom of thought, cultivates vision and encourages growth, but also inculcates human values and concern for the environment and the society. The institution is awarded with Best Chapter of ISTE AP Section.

VISION:

"To develop the SreeVaageswari Educational Society into a center of excellence in imparting professional and technical education, to inculcate knowledge and power to the younger generation by providing high quality professional education on par with global standards and to instill high levels of discipline among student community making them ethically strong citizens capable of contributing to the development of society and mankind".

PROGRAMS OFFERED:

The college offers B.Tech. programmes in CSE, EEE, ECE, Civil and Mechanical Engineering. The college also offers MCA, MBA and M.Tech. in Computer Science and Engineering.

ABOUT THE DEPARTMENT:

The department of Electronics and Communication Engineering (ECE) was started in 2005 with an intake of 60 and in a short span of time the intake increased to 180. The department has got M.Tech in ECE with an intake of 36 during this academic year (2010-11)

The department comprises of well experienced and dedicated faculty with vast experience in teaching. The faculty members have been meticulously and judiciously picked up to impart quality education and inculcate all-round development in the Students. The department has well established labs with state of art hardware equipments and advanced softwares.

The department is run by well qualified and experienced faculty under the able guidance and leadership of Mr. D.Sampath Kumar, HOD, ECE.

Consequently, the administration is trying to play the role of resources and technical leadership in development activities at the national level.

The department also seeks to provide its students with excellent educational and educational opportunities in the field of electronics and communications engineering, thus providing a lasting technical basis in an environment of rapid technical change. In the process, it enables and promotes their professional growth through contact with best professional practices. Consequently, the administration is trying to play the role of resources and technical leadership in development activities at the national level.

REGISTRATION FORM:

Please fill this form and send it to us by email to s4.ecehod@gmail.com before 30th March 2022

Name of the Participant: _____

Designation: _____

Department: _____

Organization name and Address: _____

Mobile Number: _____

Email id: _____

Signature and Seal of the
Head /Director of the Institute

NO REGISTRATION FEE



Amm
Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527



SREE VAAGESWARI EDUCATIONAL SOCIETY
VAAGESWARI COLLEGE OF ENGINEERING

(Affiliated to JNTUH, Hyderabad.)

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INVITATION LETTER

To,

Mr. Hameed Pasha Mohammad,

Associate Professor and Head,

Department of Electronics and Communication Engineering,

Jayamukhi Institute of Technological Sciences,

Warangal-506332.


Sub: Invitation to be a resource person for five day Faculty Development Program on "**Recent Trends in Microwave and Antenna Technology**"-regarding.

Dear Sir,

On behalf of Vaageswari College of Engineering, I cordially invite you to deliver a lecture on **Recent Trends in Microwave and Antenna Technology** which will be held from 04th to 08th April 2022. I would be thankful if you share your expertise on the topic with our faculty.

I will be eagerly looking forward to hear from you and hope that you would accept our invitation for the event.

Accepted & Acknowledged
MGP


Dr. Ch. Srinivas,
Principal,

Vaageswari College of Engineering,
Karimnagar.

Email: s4.principal@gmail.com

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



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INVITATION LETTER

To,

Dr. S. Sandhya Rani,

Professor,

Department of Electronics and Communication Engineering,

Jayamukhi Institute of Technological Sciences,

Warangal-506332.

Sub: Invitation to be a resource person for five day Faculty Development Program on “Recent Trends in Microwave and Antenna Technology”-regarding.

Dear Sir,

On behalf of Vaageswari College of Engineering, I cordially invite you to deliver a lecture on **Recent Trends in Microwave and Antenna Technology** which will be held from 04th to 08th April 2022. I would be thankful if you share your expertise on the topic with our faculty.

I will be eagerly looking forward to hear from you and hope that you would accept our invitation for the event.

*Accepted and
Acknowledged
Sandy*

Dr. Ch. Srinivas,
Principal,

Vaageswari College of Engineering,
Karimnagar.

Email: s4.principal@gmail.com

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



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INVITATION LETTER

To,

Mr. M. Shashidhar,

Associate Professor,

Department of Electronics and Communication Engineering,

Vaagdevi College of Engineering,

Warangal-506005.

Sub: Invitation to be a resource person for five day Faculty Development Program on **"Recent Trends in Microwave and Antenna Technology"**-regarding.

Dear Sir,

On behalf of Vaageswari College of Engineering, I cordially invite you to deliver a lecture on **Recent Trends in Microwave and Antenna Technology** which will be held from 04th to 08th April 2022. I would be thankful if you share your expertise on the topic with our faculty.

I will be eagerly looking forward to hear from you and hope that you would accept our invitation for the event.

accepted and acknowledged
[Signature]

Dr. Ch. Srinivas,
Principal,

Vaageswari College of Engineering,
Karimnagar.

Email: s4.principal@gmail.com

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



SREE VAAGESWARI EDUCATIONAL SOCIETY VAAGESWARI COLLEGE OF ENGINEERING

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Department of Electronics and Communication Engineering
Five-Day Faculty Development Program on
Recent Trends in Microwave and Antenna Technology
(04/04/2022 to 08/04/2022)

Speaker Profiles

Mr.D.Laxmi Narayana, Vaageswari College of Engineering completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Assistant Professor at Vaageswari College of Engineering. His research interests include Microwaves and antennas.

Ms.P.Ajay Kumar, completed her M.Tech from Jawaharlal Nehru Technological University. Currently she is working as an Associate Professor at Vaageswari College of Engineering. Her research interests include millimeter wave Communication.

Mr.B.Thirupathi, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Assistant Professor at Vaageswari College of Engineering. His research interests include patch Antennas.

Mr.G.S.Arun Kumar, completed his M.Tech from Jawaharlal Nehru Technological University. Currently he is working as an Assistant Professor at Vaageswari College of Engineering. His research interests include Electromagnetic devices.

Mrs.E.Jyothi, completed her M.Tech from Jawaharlal Nehru Technological University. Currently she is working as an Assistant Professor at Vaageswari College of Engineering. Her research interests include Antennas and wave propagation.

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



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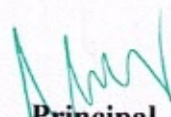
Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

SCHEDULE

Date	Morning Session (10.00 AM -12.30 AM)	Afternoon Session (1.30PM-4.00 PM)	Resource Person
Day 1 04-04-2022	Inauguration (10.00AM to 11.00PM) Introduction to Microwave Devices	Introduction to Microwave Devices (Contd.)	Mr.G.S.Arun kumar Assistant Professor, Department of ECE. Vaageswari College of Engineering
Day 2 05-04-2022	Microwave circuits and Meta materials	Millimeter waves and sub millimeter-waves	Mr.D.Laxmi Narayana, Assistant Professor, Department of ECE. Vaageswari College of Engineering
Day 3 06-04-2022	Technologies Micro strip antennas	Smart Antenna and MIMO Antenna	Ms.P.Ajay Kumar, Assistant Professor, Department of ECE. Vaageswari College of Engineering
Day 4 07-04-2022	Antenna arrays, reconfigurable antennas	Radar, imaging, sensing and localization	Mr.B.Thirupathi, Assistant. Professor, Department of ECE. Vaageswari College of Engineering
Day 5 08-04-2022	Ultra-wideband techniques Applications EMI/EMC	EMI/EMC (Contd.) Feedback and Valedictory (03.00PM to 04.00PM)	Mrs E.Jyothi, Associate Professor, Department of ECE. Vaageswari College of Engineering


Coordinator


HOD


Principal
Principal
Vaageswari College of Engineering
KARIMNAGAR-505 527.



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Department of Electronics and Communication Engineering
Five day Faculty Development Program on
Recent Trends in Microwave and Antenna Technology
(04th to 08th April 2022)

LIST OF PARTICIPANTS

S.No	NAME OF THE PARTICIPANT	SIGNATURE
1	A.VENKATA REDDY	
2	B.BHARGAVENDRA	
3	B. RAM MOHAN	
4	K.VIJAY KUMAR	
5	KALAKONDA JYOTHSNA	
6	G.PRATHEEPA	
7	JYOTHI EREDDY	
8	G.S.ARUN KUMAR	
9	D.LAXMINARAYANA	
10	THIRUPATHI BAJJURI	
11	P.AJAY KUMAR	
12	CH.PRANAY RAJ	
13	Md.KURSHID	
14	L PRAKASH	
15	NAGESWARRAO TEKULAPALLY	
16	ARUN KUMAR AKULA	

Co-Ordinator

HOD

Principal
Vaageswari College of Engineering
KARIMNAGAR-505 527



SREE VAAGESWARI EDUCATIONAL SOCIETY VAAGESWARI COLLEGE OF ENGINEERING

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(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

ATTENDANCE SHEET

S.No	NAME	04/04/2022	05/04/2022	06/04/2022	07/04/2022	08/04/2022
1	A.VENKATA REDDY	W	W	W	W	W
2	B.BHARGAVENDRA	Bgr	Bgr	Bgr	Bgr	Bgr
3	B. RAM MOHAN	Rall	Rall	Rall	Rall	Rall
4	K.VIJAY KUMAR	Vijay	Vijay	Vijay	Vijay	Vijay
5	KALAKONDA JYOTHSNA	Juof	Juof	Juof	Juof	Juof
6	G.PRATHEEPA	Parg	Parg	Parg	Parg	Parg
7	JYOTHI EREDDY	Ejri	Ejri	Ejri	Ejri	Ejri
8	G.S.ARUN KUMAR	Uff	Uff	Uff	Uff	Uff
9	D.LAXMINARAYANA	Dull	Dull	Dull	Dull	Dull
10	THIRUPATHI BAJJURI	ngb	ngb	ngb	ngb	ngb
11	P.AJAY KUMAR	P.Ajak	P.Ajak	P.Ajak	P.Ajak	P.Ajak
12	CH.PRANAY RAJ	Paj	Paj	Paj	Paj	Paj
13	Md.KURSHID	Bsd	Bsd	Bsd	Bsd	Bsd
14	L PRAKASH	Pg	Pg	Pg	Pg	Pg
15	NAGESWARRAO TEKULAPALLY	EN	EN	EN	EN	EN
16	ARUN KUMAR AKULA	Amf	Amf	Amf	Amf	Amf

Coordinator

HOD

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



SREE VAAGESWARI EDUCATIONAL SOCIETY VAAGESWARI COLLEGE OF ENGINEERING

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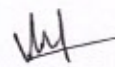
Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : A. Venkata Reddy
2. Designation & Department : Assoc Prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills				✓	
3	Self Confidence			✓		
4	Conducting class room discussions				✓	
5	Reference to the latest development		✓			
6	Linking subject to the life experience				✓	
7	Creating interest in the subject		✓			✓
8	Encouraging high order of critical thinking					
9	Encourages me to raise and make comments	✓			✓	
10	Challenges me to think				✓	
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class					
2	Knows his/her subject		✓			
3	Ability to clear areas of confusion				✓	
4	Ability to clearly explains concept					
5	Flexibility in accommodating individual participants needs	✓				
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning					✓
8	Quality of study material provided					✓
9	Quality of good reference provided				✓	
10	Time management in the class			✓		
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants			✓	✓	
2	Respecting opinion and decision of participants					
3	Willingness to learn from the participants		✓			
4	Punctuality in class			✓		
S. No.	D.) Overall Performance	1	2	3	4	5


Signature



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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : B. Bhargavendra

2. Designation & Department : Assoc prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic				✓	
2	Focus on the topic			✓		
3	Communication skills					✓
4	Self Confidence					✓
5	Conducting class room discussions				✓	
6	Reference to the latest development			✓		
7	Linking subject to the life experience				✓	
8	Creating interest in the subject					✓
9	Encouraging high order of critical thinking			✓		
10	Encourages me to raise and make comments				✓	
	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person				✓	
2	Well organized and prepared for the class				✓	
3	Knows his/her subject			✓		
4	Ability to clear areas of confusion					✓
5	Ability to clearly explains concept				✓	
6	Flexibility in accommodating individual participants needs			✓		
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning					✓
9	Quality of study material provided			✓		
10	Quality of good reference provided		✓			
	Time management in the class				✓	
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

Signature



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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : B. Ram mohan
2. Designation & Department : Asst Prof

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic					✓
2	Focus on the topic					✓
3	Communication skills				✓	
4	Self Confidence			✓		
5	Conducting class room discussions			✓		
6	Reference to the latest development			✓		
7	Linking subject to the life experience		✓			
8	Creating interest in the subject				✓	
9	Encouraging high order of critical thinking					✓
10	Encourages me to raise and make comments				✓	
11	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					✓
2	Well organized and prepared for the class					✓
3	Knows his/her subject				✓	
4	Ability to clear areas of confusion				✓	
5	Ability to clearly explains concept		✓			
6	Flexibility in accommodating individual participants needs			✓		
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning				✓	
9	Quality of study material provided				✓	
10	Quality of good reference provided				✓	
11	Time management in the class					✓
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants		✓			
3	Willingness to learn from the participants					✓
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5

Ram
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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : K. Vijay Kumar

2. Designation & Department : Asst. Prof., ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic			✓		
2	Communication skills					✓
3	Self Confidence				✓	
4	Conducting class room discussions				✓	✓
5	Reference to the latest development					✓
6	Linking subject to the life experience			✓		
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments					✓
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class			✓		
2	Knows his/her subject					✓
3	Ability to clear areas of confusion				✓	
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs					✓
6	Uses effective teaching method to enhance learning			✓		
7	Contributes to improve my learning				✓	
8	Quality of study material provided					✓
9	Quality of good reference provided		✓			
10	Time management in the class			✓		
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants					✓
3	Willingness to learn from the participants				✓	
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : K. Jyothsna
2. Designation & Department : Asst. Prof., ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills			✓		
3	Self Confidence				✓	
4	Conducting class room discussions		✓			
5	Reference to the latest development					✓
6	Linking subject to the life experience			✓		
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking					✓
9	Encourages me to raise and make comments				✓	
10	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject			✓		
3	Ability to clear areas of confusion				✓	
4	Ability to clearly explains concept		✓			
5	Flexibility in accommodating individual participants needs				✓	
6	Uses effective teaching method to enhance learning		✓			
7	Contributes to improve my learning					✓
8	Quality of study material provided			✓		
9	Quality of good reference provided				✓	
10	Time management in the class			✓		
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants					✓
4	Punctuality in class			✓		
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : *Gr. Pratheepa*
2. Designation & Department : *Asst. prof, ECE*

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic					✓
2	Communication skills				✓	
3	Self Confidence			✓		
4	Conducting class room discussions					✓
5	Reference to the latest development				✓	
6	Linking subject to the life experience			✓		
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments			✓		
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class				✓	
2	Knows his/her subject			✓		
3	Ability to clear areas of confusion					✓
4	Ability to clearly explains concept				✓	
5	Flexibility in accommodating individual participants needs			✓		
6	Uses effective teaching method to enhance learning					✓
7	Contributes to improve my learning				✓	
8	Quality of study material provided			✓		
9	Quality of good reference provided					✓
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : E. Syathi
2. Designation & Department : Assoc Prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic			✓		
2	Focus on the topic				✓	
3	Communication skills					✓
4	Self Confidence			✓		
5	Conducting class room discussions				✓	
6	Reference to the latest development				✓	
7	Linking subject to the life experience				✓	
8	Creating interest in the subject					✓
9	Encouraging high order of critical thinking			✓		
10	Encourages me to raise and make comments		✓			
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person			✓		
2	Well organized and prepared for the class					✓
3	Knows his/her subject			✓		
4	Ability to clear areas of confusion				✓	
5	Ability to clearly explains concept					✓
6	Flexibility in accommodating individual participants needs				✓	
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning			✓		
9	Quality of study material provided					✓
10	Quality of good reference provided				✓	
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants					✓
3	Willingness to learn from the participants				✓	
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

E. Syathi
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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : G.S. Arun kumar

2. Designation & Department : Asst. prof, ECE

The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic				✓	
2	Focus on the topic					✓
3	Communication skills			✓		
4	Self Confidence					✓
5	Conducting class room discussions		✓			
6	Reference to the latest development				✓	
7	Linking subject to the life experience					✓
8	Creating interest in the subject					✓
9	Encouraging high order of critical thinking				✓	
10	Encourages me to raise and make comments				✓	
11	Challenges me to think				✓	
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person				✓	
2	Well organized and prepared for the class			✓		
3	Knows his/her subject				✓	
4	Ability to clear areas of confusion		✓			
5	Ability to clearly explains concept					✓
6	Flexibility in accommodating individual participants needs			✓		
7	Uses effective teaching method to enhance learning					✓
8	Contributes to improve my learning			✓		
9	Quality of study material provided				✓	
10	Quality of good reference provided					✓
11	Time management in the class					✓
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants					✓
4	Punctuality in class			✓		
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : D. Laxminarayana
2. Designation & Department : Asst. prof

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic					✓
2	Focus on the topic					✓
3	Communication skills		✓			
4	Self Confidence			✓		
5	Conducting class room discussions				✓	
6	Reference to the latest development				✓	
7	Linking subject to the life experience				✓	
8	Creating interest in the subject					✓
9	Encouraging high order of critical thinking			✓		
10	Encourages me to raise and make comments				✓	
11	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					✓
2	Well organized and prepared for the class					✓
3	Knows his/her subject				✓	
4	Ability to clear areas of confusion		✓			
5	Ability to clearly explains concept				✓	
6	Flexibility in accommodating individual participants needs				✓	
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning					✓
9	Quality of study material provided					✓
10	Quality of good reference provided			✓		
11	Time management in the class			✓		
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants		✓			
3	Willingness to learn from the participants					✓
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5


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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : B. Thimulathur
2. Designation & Department : Asst. prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic					✓
2	Focus on the topic					
3	Communication skills					
4	Self Confidence			✓	✓	
5	Conducting class room discussions				✓	
6	Reference to the latest development					✓
7	Linking subject to the life experience				✓	
8	Creating interest in the subject			✓		
9	Encouraging high order of critical thinking		✓			
10	Encourages me to raise and make comments		✓			
11	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					
2	Well organized and prepared for the class					✓
3	Knows his/her subject				✓	
4	Ability to clear areas of confusion			✓		
5	Ability to clearly explains concept				✓	
6	Flexibility in accommodating individual participants needs					✓
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning			✓		
9	Quality of study material provided		✓			
10	Quality of good reference provided			✓		
11	Time management in the class				✓	
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : P. Ajay Kumar

2. Designation & Department : Asst - prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic					✓
2	Focus on the topic					✓
3	Communication skills				✓	
4	Self Confidence			✓		
5	Conducting class room discussions					✓
6	Reference to the latest development					✓
7	Linking subject to the life experience		✓			
8	Creating interest in the subject				✓	
9	Encouraging high order of critical thinking			✓		
10	Encourages me to raise and make comments				✓	
11	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					
2	Well organized and prepared for the class			✓		
3	Knows his/her subject				✓	
4	Ability to clear areas of confusion		✓			
5	Ability to clearly explains concept				✓	
6	Flexibility in accommodating individual participants needs					✓
7	Uses effective teaching method to enhance learning					✓
8	Contributes to improve my learning			✓		
9	Quality of study material provided				✓	
10	Quality of good reference provided			✓		
11	Time management in the class					✓
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants					✓
4	Punctuality in class			✓		
S. No.	D.) Overall Performance	1	2	3	4	5

P. Ajay Kumar
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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : *Ch. Pranay Raj*
2. Designation & Department : *Asst prof, ECE*

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic				✓	
2	Focus on the topic					✓
3	Communication skills			✓		
4	Self Confidence					✓
5	Conducting class room discussions		✓			
6	Reference to the latest development			✓		
7	Linking subject to the life experience					✓
8	Creating interest in the subject				✓	
9	Encouraging high order of critical thinking			✓		
10	Encourages me to raise and make comments		✓			
11	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					✓
2	Well organized and prepared for the class				✓	
3	Knows his/her subject			✓		
4	Ability to clear areas of confusion				✓	
5	Ability to clearly explains concept		✓			
6	Flexibility in accommodating individual participants needs				✓	
7	Uses effective teaching method to enhance learning					✓
8	Contributes to improve my learning			✓		
9	Quality of study material provided				✓	
10	Quality of good reference provided					✓
11	Time management in the class			✓		
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants					✓
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants			✓		
4	Punctuality in class		✓			
S. No.	D.) Overall Performance	1	2	3	4	5

Raj
Signature



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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : *md. khurshid*
2. Designation & Department : *Asst. prof, ECE*

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
	How well the Resource Person focus on the topic					
1	Focus on the topic				✓	
2	Communication skills					✓
3	Self Confidence				✓	
4	Conducting class room discussions			✓		
5	Reference to the latest development		✓			
6	Linking subject to the life experience				✓	
7	Creating interest in the subject					✓
8	Encouraging high order of critical thinking				✓	
9	Encourages me to raise and make comments			✓		
10	Challenges me to think		✓			
S. No.	B.) Content	1	2	3	4	5
	Depth of knowledge of the Resource Person					
1	Well organized and prepared for the class					✓
2	Knows his/her subject				✓	
3	Ability to clear areas of confusion					✓
4	Ability to clearly explains concept			✓		
5	Flexibility in accommodating individual participants needs		✓			
6	Uses effective teaching method to enhance learning				✓	
7	Contributes to improve my learning					✓
8	Quality of study material provided				✓	
9	Quality of good reference provided			✓		
10	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
	Ability to listen and understand participants				✓	
2	Respecting opinion and decision of participants					✓
3	Willingness to learn from the participants			✓		
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5

md
Signature



SREE VAAGESWARI EDUCATIONAL SOCIETY VAAGESWARI COLLEGE OF ENGINEERING

(Affiliated to JNTUH, Hyderabad.)

(Approved by AICTE New Delhi & Recognised by the Govt. of Telangana State)

Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : L. Prakash
2. Designation & Department : Asst. prof

• The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

• Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic					
2	Focus on the topic				✓	
3	Communication skills				✓	
4	Self Confidence					✓
5	Conducting class room discussions			✓		
6	Reference to the latest development		✓			
7	Linking subject to the life experience				✓	
8	Creating interest in the subject				✓	
9	Encouraging high order of critical thinking					✓
10	Encourages me to raise and make comments			✓		
11	Challenges me to think			✓		
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					
2	Well organized and prepared for the class					✓
3	Knows his/her subject					✓
4	Ability to clear areas of confusion				✓	
5	Ability to clearly explains concept		✓			
6	Flexibility in accommodating individual participants needs			✓		
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning					✓
9	Quality of study material provided				✓	
10	Quality of good reference provided			✓		
11	Time management in the class			✓		
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants			✓		
2	Respecting opinion and decision of participants			✓		
3	Willingness to learn from the participants					✓
4	Punctuality in class				✓	
S. No.	D.) Overall Performance	1	2	3	4	5

Signature



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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : T. Nageshwar Rao
2. Designation & Department : Assoc Prof, ECE

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent

- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic				✓	
2	Focus on the topic					✓
3	Communication skills				✓	
4	Self Confidence			✓		
5	Conducting class room discussions				✓	
6	Reference to the latest development		✓			
7	Linking subject to the life experience			✓		
8	Creating interest in the subject				✓	
9	Encouraging high order of critical thinking				✓	
10	Encourages me to raise and make comments					✓
11	Challenges me to think				✓	
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					
2	Well organized and prepared for the class			✓		
3	Knows his/her subject		✓			
4	Ability to clear areas of confusion			✓		
5	Ability to clearly explains concept				✓	
6	Flexibility in accommodating individual participants needs					✓
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning					✓
9	Quality of study material provided				✓	
10	Quality of good reference provided			✓		
11	Time management in the class		✓			
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants				✓	
2	Respecting opinion and decision of participants					✓
3	Willingness to learn from the participants				✓	
4	Punctuality in class			✓		
S. No.	D.) Overall Performance	1	2	3	4	5
				✓		

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Department of Electronics and Communication Engineering Five day Faculty Development Program on Recent Trends in Microwave and Antenna Technology (04th to 08th April 2022)

FEEDBACK

1. Name of the Participant : A. Arunkumar
2. Designation & Department : Asst prof

- The numbers rating are: 1. Very Bad 2. Bad 3. Good 4. Very Good 5. Excellent
- Tick (✓) the answer that fits your experience. Your sincere feedback will help the centre to improve service delivery.

S. No.	A.) Subject Command	1	2	3	4	5
1	How well the Resource Person focus on the topic					✓
2	Focus on the topic					✓
3	Communication skills					✓
4	Self Confidence				✓	
5	Conducting class room discussions		✓	✓		
6	Reference to the latest development		✓			
7	Linking subject to the life experience				✓	
8	Creating interest in the subject				✓	
9	Encouraging high order of critical thinking			✓		
10	Encourages me to raise and make comments				✓	
	Challenges me to think					✓
S. No.	B.) Content	1	2	3	4	5
1	Depth of knowledge of the Resource Person					
2	Well organized and prepared for the class			✓		
3	Knows his/her subject					✓
4	Ability to clear areas of confusion			✓		
5	Ability to clearly explains concept				✓	
6	Flexibility in accommodating individual participants needs		✓			
7	Uses effective teaching method to enhance learning				✓	
8	Contributes to improve my learning			✓		
9	Quality of study material provided					✓
10	Quality of good reference provided				✓	
	Time management in the class				✓	
S. No.	C.) Delivery	1	2	3	4	5
1	Ability to listen and understand participants		✓			
2	Respecting opinion and decision of participants				✓	
3	Willingness to learn from the participants				✓	
4	Punctuality in class					✓
S. No.	D.) Overall Performance	1	2	3	4	5

Arunkumar
Signature



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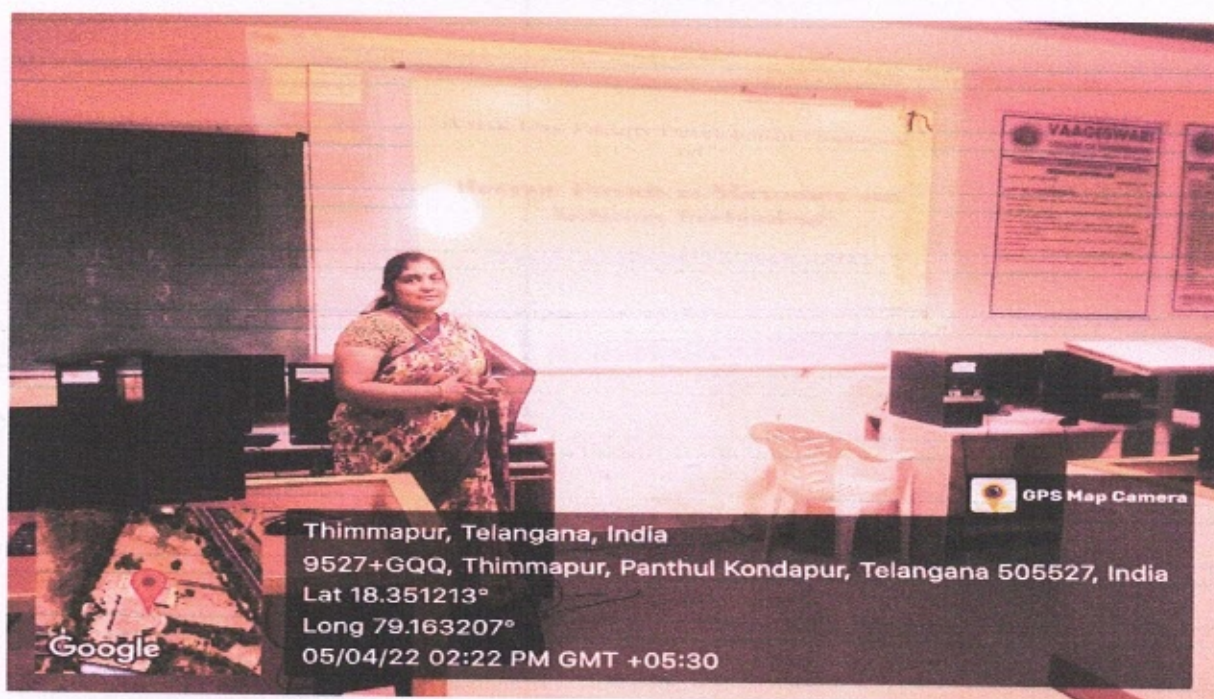
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Department of Electronics and Communication Engineering

Five day Faculty Development Program on

Recent Trends in Microwave and Antenna Technology

(04th to 08th April 2022)



Mrs.E.Jyothi, Associate Professor, Department of ECE delivering a lecture on Antennas.

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.



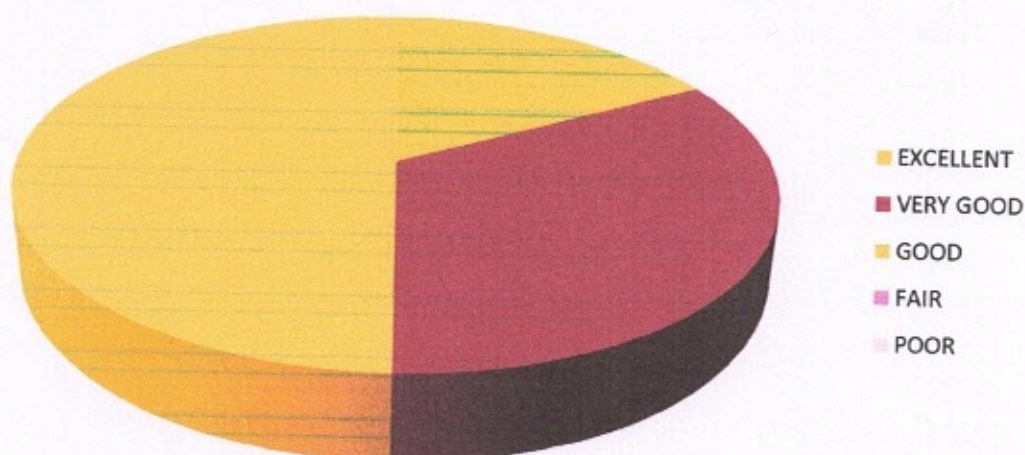
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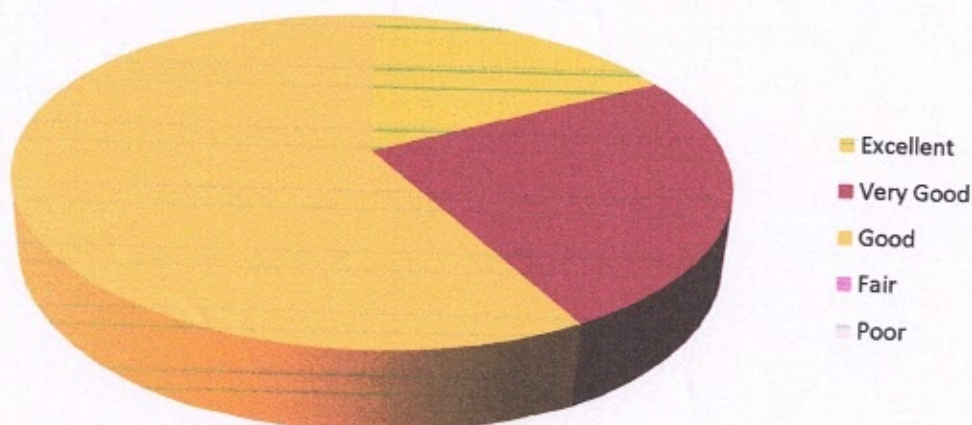
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Department of Electronics and Communication Engineering
Five day Faculty Development Program on
RECENT TRENDS IN MICROWAVE AND ANTENNA TECHNOLOGY
(04th to 08th April 2022)
ANALYSIS

**The information presented was relevant to
your needs and expectations**



**Sufficient opportunity was provided for
questions**



Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527.

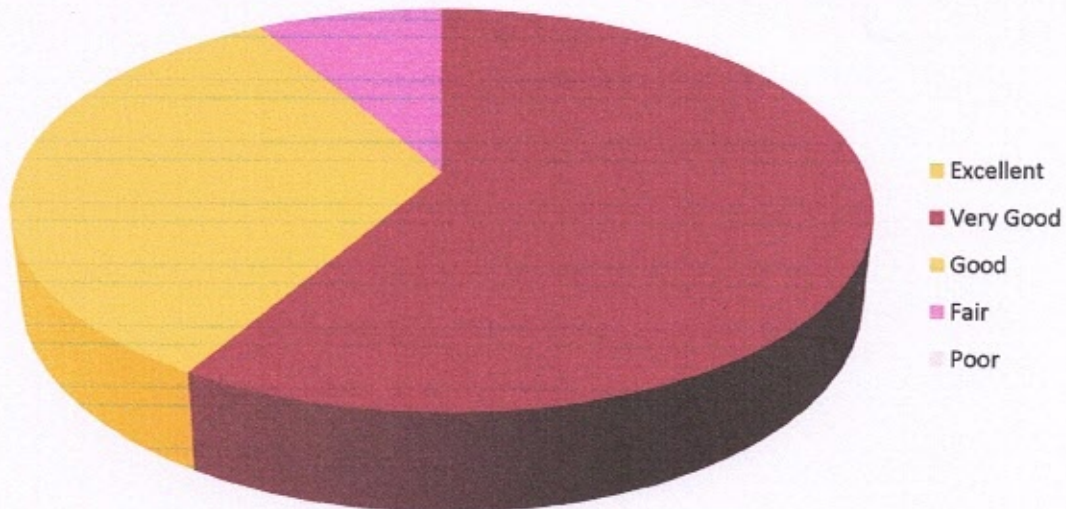


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You were pleased with the presentation



ACTION TAKEN

The organizing committee has taken a decision to conduct the upcoming Faculty Development Programmes in a very effective and efficient manner. The committee also decided to invite speakers from very reputed organizations. It has been decided to make the programmes more of practical oriented and more focus is to be put on latest and emerging methodologies which would make the staff members to be aware of the latest trending methods pertaining to latest electronics and communication advancements like 5G Technology and IoT. The committee decided to organize more programmes on new emerging trends in Electronics and Communication Engineering.

A handwritten signature in green ink, likely belonging to the Principal, is positioned above the printed name.

Principal

Vaageswari College of Engineering
KARIMNAGAR-505 527,



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Department of Electronics and Communication Engineering

Five-Day Faculty Development Program on

Recent Trends in Microwave and Antenna Technology

(04/04/2024 to 08/04/2024)

Report

A five Day Faculty Development Program on “Recent Trends in Microwave and Antenna Technology” was organized by Department of Electronics and Communication Engineering, Vaageswari college of Engineering from 13th to 17th September 2021 for the faculty of ECE Department. Several faculty members of the Department actively participated and gained useful information from all the sessions.

The program intends to present the methodology for the design and development of the components at Microwave and Millimeter Wave Frequencies. The program will include the various aspects of microstrip antennas and the state of the art developments that have taken place in the recent time. It opens up opportunity to highlight the design consideration and application of the reconfigurable antennas in the realm of wireless communication. The course will address the issues like EMI/EMC and the measurement of the radiated electromagnetic field quantities to ensure the satisfactory performances of the electrical or electronic equipment's in Electromagnetic Environment.

Outcomes of the Faculty Development Program:

DAY 1(04/04/2024)


Mr.G.S Arun Kumar, Assistant Professor, Department of ECE,Vaageswari College of Engineering explained Microwave devices and antennas. Devices.

DAY2 (05/04/2024)

Mr.D.LaxmiNarayana, Assistant Professor, Department of ECE, Vaageswari College of Engineering explained about Microwave circuits and Meta materials and Millimeter waves and sub millimeter-waves.

DAY 3(06/04/2024)

Ms.P.Ajay Kumar, AssistantProfessor, Department of ECE, Vaageswari College of Engineering explained about Technologies Micro strip antennas Smart Antenna and MIMO antenna.


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DAY 4(07/04/2024)

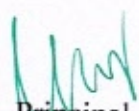
Ms.B.Thirupathi AssistantProfessor, Department of ECE, Vaageswari College of Engineering explained about Antenna arrays, reconfigurable antennas and Radar imaging, sensing and localization.

DAY 5(08/04/2024)

Mrs.E.Jyothi, Associate Professor, Department of ECE, Vaageswari College of Engineering Ultra-wideband techniques Applications EMI/EMC and organized valedictory function.


Coordinator


HOD


Principal
Principal
Vaageswari College of Engineering
KARIMNAGAR-505 527.